

FIG. 1 is a schematic diagram of a multi-processor system 1. The system includes three processing units, each comprising a CPU (CPU₁, CPU₂, CPU₃) and an I/O block (I/O₁, I/O₂, I/O₃). These units are connected to a common Bus (5) via I/O blocks (6). The system also includes memory (7) and a control unit (8). The diagram shows the internal structure of the system, including the CPU and I/O blocks, and their connection to the Bus. The system is labeled 1, and the components are labeled with numbers 2 through 11. The diagram is a schematic representation of a multi-processor system.

FIG. 1

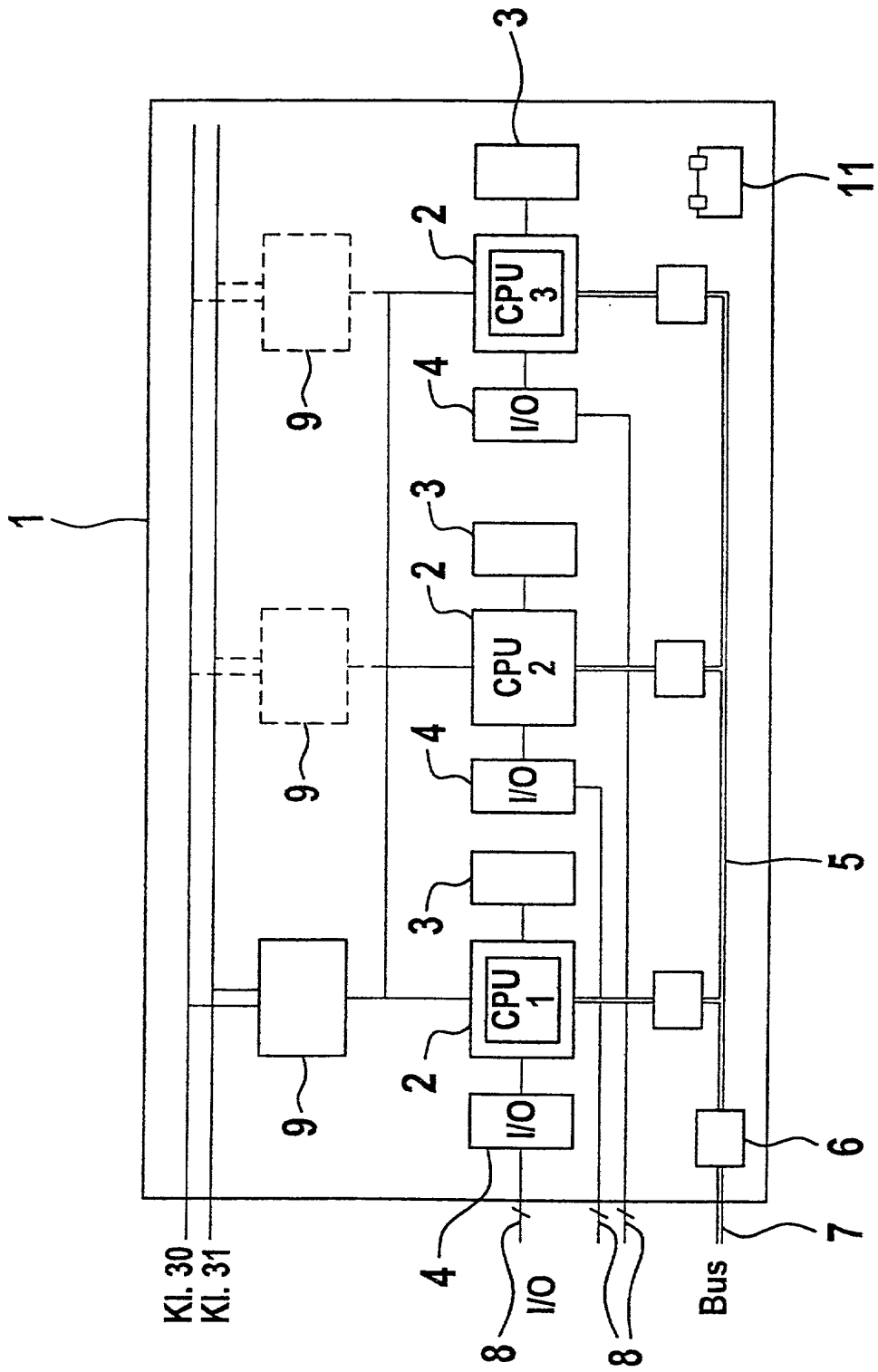


FIG. 2

